

ASHOKA INSTITUTE OF TECHNOLOGY & MANAGEMENT, VARANASI

Department of Computer Science and Engineering

B.Tech CSE-II Year (Odd Semester TIME TABLE - 2024-2025)

w.e.f : 18 Nov 2024

Section -A

Day/Time	9:00-09:55	9:55-10:50	10:50-11:45	11:45-12:40	12:40-01:25	01:25-02:15	02:15-03:05	03:05-04:00
Mon	Data Structure	Discrete Structures & Theory of Logic	Mini Project or Internship Assessment		L U N C H	Python Programming	Universal Human Value	Digital Electronics
Tues	Discrete Structures & Theory of Logic	Computer Organization and Architecture	Digital Electronics	Library		Python Programming	Data Structure	Digital Electronics Group -1 (T) / Discrete Structures & Theory of Logic Group -2
Wed	Web Designing Workshop		Digital Electronics	PDP (Soft Skill)		Data Structure	PDP (Technical)	Universal Human Value
Thurs	Data Structure	Discrete Structures & Theory of Logic	Mentor Mentee Interaction	Computer Organization and Architecture		Python Programming	Computer Organization and Architecture Lab	
Fri	KCS351: Data Structure Using C Lab		Discrete Structures & Theory of Logic	Computer Organization and Architecture		Data Structure Group 1 (T)/Computer Organization and Architecture Group -2 (T)	Web Designing Workshop	PDP (Aptitude)
Sat	Digital Electronics	Universal Human Value	Digital Electronics Group -2 (T) / Discrete Structures & Theory of Logic Group -1	Data Structure		Data Structure	Library	Data Structure Group 2 (T)/Computer Organization and Architecture Group -1 (T)
	Group -1: - 2306410100001 to 2306410100031							
	Group -2: - 2306410100032 to 2306410100062							
S.No.	Code and Subject Name			Faculty Name				
1	BOE310: Digital Electronics			Dr. Arvind Kumar Maurya				
2	BVE301: Universal Human Value			Mr. Gaurav Singh				
3	BCS301: Data Structure			Mr. Deeksha Sharma				
4	BCS302: Computer Organization and Architecture			Mr. Pankaj Kumar				
5	BCS303: Discrete Structures & Theory of Logic			Ms. Kavita Patel				
6	BCC302: Python Programming			Mr. Abhay Kumar Maurya				
7	BCS351: Data Structures Lab			Mr. Deeksha Vishwakarma				
8	BCS352: Computer Organization and Architecture Lab			Mr. Pankaj Kumar				
9	BCS353: Web Designing Workshop			Mr. Ankur Srivastava				
10	Mini Project or Internship Assessment*			Ms. Alka Singh				
11	PDP			Training & Placement Dept.				
12	Mentor Mentee Interaction Group			Respective Mentor				
	Roll No :	01 to 23		Ms. Diksha Sharma				
	Roll No :	24 to 47		Mr. Sandeep Singh				
	Roll No :	48 to 71		Mr. Arvind Kumar				

Dr. Sarika Shrivastava
(Director)

Mr. Pankaj Kumar Srivastava
(Time Table Coordinator)

Ms. Alka Singh
(Class Coordinator)

Dr. Priti Kumari
(Head of Department, CSE)

ASHOKA INSTITUTE OF TECHNOLOGY & MANAGEMENT, VARANASI

Department of Computer Science and Engineering

B.Tech CSE-II Year (Odd Semester TIME TABLE - 2024-2025)

w.e.f : 18 Nov 2024

Section -B

Day/Time	9:00-09:55	9:55-10:50	10:50-11:45	11:45-12:40	12:40-01:25	01:25-02:15	02:15-03:05	03:05-04:00
Mon	Web Designing Workshop		Data Structure	Python Programming	L U N C H	Digital Electronics	Python Programming	Discrete Structures & Theory of Logic
Tues	Digital Electronics	Data Structure	Mini Project or Internship Assessment			Mentor Mentee Interaction	Python Programming	Universal Human Value
Wed	Computer Organization and Architecture	Discrete Structures & Theory of Logic	Data Structure	PDP (Aptitude)		Digital Electronics Group - 1 (T) / Discrete Structures & Theory of Logic Group-2	Universal Human Value	Data Structure Group 1 (T) / Computer Organization and Architecture Group -2 (T)
Thurs	Computer Organization and Architecture	Universal Human Value	KCS351: Data Structure Using C Lab			Digital Electronics	PDP (Soft Skill)	Discrete Structures & Theory of Logic
Fri	Discrete Structures & Theory of Logic	Web Designing Workshop	Python Programming			Computer Organization and Architecture Lab		Digital Electronics
Sat	Data Structure	Computer Organization and Architecture	Python Programming	Data Structure Group 2 (T) / Computer Organization and Architecture Group -1 (T)		Digital Electronics Group - 2 (T) / Discrete Structures & Theory of Logic Group-1	Data Structure	

S.No.	Code and Subject Name	Faculty Name
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Note: Classes will be suspended on 1st and 3rd Saturday of every Month

Group-1: 2306410100072 to Lateral Entry (CS)

Group-2: 2306411530001 to Lateral Entry (AI & ML)

S.No.	Code and Subject Name	Faculty Name
1	BOE310: Digital Electronics	Dr. Arvind Kumar Maurya
2	BVE301: Universal Human Value	Mrs. Deeksha Sharma
3	BCS301: Data Structure	Mrs. Deeksha Sharma
4	BCS302: Computer Organization and Architecture	Mr. Pankaj Kumar
5	BCS303: Discrete Structures & Theory of Logic	Ms. Kavita Patel
6	BCC302: Python Programming	Mr. Jai Prakash Singh
7	BCS351: Data Structures Lab	Mrs. Deeksha Vishwakarma
8	BCS352: Computer Organization and Architecture Lab	Mr. Pankaj Kumar
9	BCS353: Web Designing Workshop	Mr. Sandeep Singh
10	Mini Project or Internship Assessment*	Mr. Gaurav Singh
11	PDP	Training & Placement Dept.
12	Mentor Mentee Interaction Group	Respective Mentor
	Roll No : 72 to 93	Mr. Pankaj Kumar Srivastava
	Roll No : 94 + Lateral + (AI & ML) 01 to 14	Mr. Gaurav Singh
	Roll No : 15 to 32 (AI & ML) + Lateral	Mr. Pankaj Kumar

Dr. Sarika Shrivastava
(Director)

Mr. Pankaj Kumar Srivastava
(Class Coordinator / Time Table Coordinator)

Dr. Priti Kumari
(Head of Department, CSE)